

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	18	(William near2 Sauber).in.	US-PGPUB; USPAT	OR	ON	2005/12/02 13:53
L13	2	((video near2 controller) and switch\$3 and (chip chipset integrated) and agp).clm.	US-PGPUB; USPAT	OR	ON	2005/12/02 13:54

Interference Search

H. Nguyen 12/05/2005

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8765	agp (accelerat\$3 adj graphic\$2 adj port)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:18
L2	51380	(graphics video) near (card adapter accelerator controller processor engine hardware chip chipset)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:20
L3	5544	1 and 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:20
L4	8498	(external add-in plug-in add\$3 plug\$5 second auxiliary optional) with ((graphics video) near (card adapter accelerator controller processor engine hardware chip chipset))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:22
L5	1707	1 and 4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:22
L6	6	5 and (low near resistance)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:22
L8	261	345/503.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:40
L9	174	8 and (chip chipset integrated)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:41
L11	18	(William near2 Sauber).in.	US-PGPUB; USPAT	OR	ON	2005/12/02 13:53
L13	2	((video near2 controller) and switch\$3 and (chip chipset integrated) and agp).clm.	US-PGPUB; USPAT	OR	ON	2005/12/02 13:54
S2	462	345/520	US-PGPUB; USPAT	OR	ON	2004/07/07 11:13
S3	20	video and (scalable near platform)	US-PGPUB; USPAT	OR	ON	2003/03/04 15:07
S4	1	345/520 and ((first and second) near video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/04 15:38
S11	772	345/132	US-PGPUB; USPAT	OR	ON	2003/03/04 16:45
S12	2	345/132 and (((first and second) or multiple) near video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/04 17:14
S18	278	345/1.1	US-PGPUB; USPAT	OR	ON	2003/03/05 10:35
S19	8	345/1.1 and (multiple near3 controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 10:45
S20	129	345/3.1	US-PGPUB; USPAT	OR	ON	2003/03/05 10:35
S21	180	(multiple near3 controller) and (chip adj set)	US-PGPUB; USPAT	OR	ON	2003/03/05 10:45
S22	1	(multiple near video near controller) and (chip adj set)	US-PGPUB; USPAT	OR	ON	2003/03/05 10:51
S23	3	(multiple near video near controller) same (chip)	US-PGPUB; USPAT	OR	ON	2003/03/05 11:07

S24	1076	(agg near (socket or port))	US-PGPUB; USPAT	OR	ON	2003/03/05 14:10
S25	1	((agg near (socket or port))) and ((first and second) near video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 11:09
S26	127	((agg near (socket or port))) and (video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 11:09
S27	2	((agg near (socket or port)) same (video near controller)) and (chip adj set)	US-PGPUB; USPAT	OR	ON	2003/03/05 14:11
S28	63	(agg near (socket or port)) same (video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 14:22
S29	41	(accelerated adj graphics adj port) same (video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 14:25
S30	489	(accelerated adj graphics adj port) and intel	US-PGPUB; USPAT	OR	ON	2003/03/05 14:26
S31	374	(accelerated adj graphics adj port) and (intel near corporation)	US-PGPUB; USPAT	OR	ON	2003/03/05 14:29
S32	73	((accelerated adj graphics adj port) and (intel near corporation)) and (video near controller)	US-PGPUB; USPAT	OR	ON	2003/03/05 14:35
S35	65	345/532	US-PGPUB; USPAT	OR	ON	2003/03/05 15:39
S37	121	(multiple near display) and (video adj2 controller) and chip\$4	US-PGPUB; USPAT	OR	ON	2004/02/05 10:22
S38	99	((multiple near display) and (video adj2 controller) and chip\$4) and connect\$3	US-PGPUB; USPAT	OR	ON	2004/02/05 10:22
S39	80	((multiple near display) and (video adj2 controller) and chip\$4) and connect\$3 and switch\$4	US-PGPUB; USPAT	OR	ON	2004/02/05 10:39
S42	1370	(first and second) with ((video display) adj2 controller)	US-PGPUB; USPAT	OR	ON	2004/02/05 10:57
S43	530	((first and second) with ((video display) adj2 controller)) and (chip or (integrated adj2 circuit))	US-PGPUB; USPAT	OR	ON	2004/02/05 10:58
S44	534	((first and second) with ((video display) adj2 controller)) and (chip\$4 or (integrated adj2 circuit))	US-PGPUB; USPAT	OR	ON	2004/02/05 10:58
S45	375	((first and second) with ((video display) adj2 controller)) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3)	US-PGPUB; USPAT	OR	ON	2004/02/05 10:58
S46	18	((first and second) with ((video display) adj2 controller)) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3) and ((first and second) near2 connector)	US-PGPUB; USPAT	OR	ON	2004/07/07 11:14
S50	143	((first and second) with ((video display) adj2 controller)) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3) and connector	US-PGPUB; USPAT	OR	ON	2004/02/05 16:38
S63	69	345/778	US-PGPUB; USPAT	OR	ON	2004/02/05 16:55
S64	101	345/717	US-PGPUB; USPAT	OR	ON	2004/02/05 16:56
S65	19	345/717 and ((display video graphics) near2 controller)	US-PGPUB; USPAT	OR	ON	2004/02/05 16:56
S66	17	((first and second) with ((video display) adj2 controller)) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3) and agg	US-PGPUB; USPAT	OR	ON	2004/07/07 11:38
S67	8	((first and second) or (multiple plural)) adj5 ((video display) adj2 controller) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3) and agg	US-PGPUB; USPAT	OR	ON	2004/07/07 11:39
S68	154	((first and second) or (multiple plural)) adj5 ((video display) adj2 controller) and (chip\$4 or (integrated adj2 circuit))	US-PGPUB; USPAT	OR	ON	2004/07/07 13:28
S69	12	((first and second) or (multiple plural)) adj5 ((video display) adj2 controller) and (chip\$4 or (integrated adj2 circuit)) and (switch\$3 adj2 circuit)	US-PGPUB; USPAT	OR	ON	2004/07/07 13:27
S70	163	(external adj3 ((video display) near controller))	US-PGPUB; USPAT	OR	ON	2004/07/07 13:28

S71	90	((external adj3 ((video display) near controller))) and (chip\$4 or (integrated adj2 circuit))	US-PGPUB; USPAT	OR	ON	2004/07/07 13:28
S72	12	((external adj3 ((video display) near controller))) and (chip\$4 or (integrated adj2 circuit)) and (agp or (accelarated adj2 graphics adj port))	US-PGPUB; USPAT	OR	ON	2004/07/07 13:40
S73	508	chip\$4 with agp	US-PGPUB; USPAT	OR	ON	2004/07/07 13:40
S74	210	(chip\$4 with agp) and ((video graphics display) adj3 controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 13:41
S75	178	((chip\$4 with agp) and ((video graphics display) adj3 controller)) and (switch\$3 select\$3)	US-PGPUB; USPAT	OR	ON	2004/07/07 14:16
S78	13851	(chipset or (system adj2 chip))	US-PGPUB; USPAT	OR	ON	2004/07/07 14:16
S79	3731	((first and second) or (multiple plurality)) with ((video graphics display) adj3 controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 14:17
S80	211	((chipset or (system adj2 chip))) and (((first and second) or (multiple plurality)) with ((video graphics display) adj3 controller))	US-PGPUB; USPAT	OR	ON	2004/07/07 14:17
S81	75	((chipset or (system adj2 chip))) and (((first and second) or (multiple plurality)) with ((video graphics display) adj3 controller)) and (agp or (accelerated adj graphics adj port))	US-PGPUB; USPAT	OR	ON	2004/07/07 14:20
S82	1	09/768073	US-PGPUB; USPAT	OR	ON	2004/07/07 14:21
S83	22	((switch\$3 or select\$3) adj2 device) with (video near3 controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 14:56
S84	87	((switch\$3 or select\$3) adj2 (device circuit)) with (video near3 controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 14:56
S85	7	"5471316".URPN.	USPAT	OR	ON	2004/07/07 15:24
S86	206	(chipset or (system adj2 chip)) and agp and ((i adj o) adj2 control\$4) and (memory adj control\$4)	US-PGPUB; USPAT	OR	ON	2004/07/07 15:47
S88	167	((chipset or (system adj2 chip)) and agp and ((i adj o) adj2 control\$4) and (memory adj control\$4)) and ((video display graphics) adj2 controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 15:43
S89	217	(chipset or (system adj2 chip)) and (agp (accelerated adj graphics adj port)) and ((i adj o) adj2 control\$4) and (memory adj control\$4)	US-PGPUB; USPAT	OR	ON	2004/07/07 15:51
S90	65	(chipset or (system adj2 chip)) same ((agp (accelerated adj graphics adj port)) and ((i adj o) adj2 control\$4) and (memory adj control\$4))	US-PGPUB; USPAT	OR	ON	2004/07/07 16:10
S91	404	345/519	US-PGPUB; USPAT	OR	ON	2004/07/07 16:10
S92	6	345/519 and (agp (accelerated adj graphics adj port)) and ((i adj o) adj2 control\$4) and (memory adj control\$4)	US-PGPUB; USPAT	OR	ON	2004/07/07 16:11
S93	5	("6292859"   "6321287"   "6532019"   "6624817"   "6630936").PN.	USPAT	OR	ON	2004/07/07 16:14
S94	7	("5640578"   "5919256"   "5940087"   "5943064"   "5995121"   "6008823"   "6389504").PN.	USPAT	OR	ON	2004/07/07 16:18
S95	4453	712/1-43.ccls.	US-PGPUB; USPAT	OR	ON	2004/07/07 16:20
S96	192	712/1-43.ccls. and ((graphics video display) adj controller)	US-PGPUB; USPAT	OR	ON	2004/07/07 16:20
S97	49	(712/1-43.ccls. and ((graphics video display) adj controller)) and (agp (accelerated adj graphics adj port))	US-PGPUB; USPAT	OR	ON	2004/07/07 16:20
S100	1847	(first and second) near2 ((display video) near2 (control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 07:59

S10 1	422	S100 and (chip (integrated near circuit) chipset)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:04
S10 2	10	S101 and agp	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:04
S10 3	1847	(first and second) near2 ((display video) near2 (control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:33
S10 4	422	S103 and (chip (integrated near circuit) chipset)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:32
S10 5	1	((first and second) near2 ((display video) near2 (control\$4))) same agp	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:36
S10 6	30	"6254481"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:35
S10 7	332	((display video) near2 control\$4) with (agp (accelerat\$3 near graphic\$2 near port))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:37
S10 8	145	S107 and (switch\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/09 08:39
S11 5	1	((analog and digital) near2 display)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 11:04
S11 6	4023	((analog and digital) near2 display)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 11:04
S11 7	61	S116 and agp	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 11:04
S11 9	31	((crt and dvi) near3 display) and (selector (selecting near2 circuit) switch\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 12:56
S12 0	4115	((crt and dvi) (digital and analog )) near3 display) and (selector (selecting near2 circuit) switch\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 12:57
S12 1	24	S120 and ((first and second) near3 ((graphics video) near2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/03/10 12:58
S12 4	17	("5237690"   "5321806"   "5574948"   "5592678"   "5623628"   "5630174"   "5680625"   "5768612"   "5799204"   "5857083"   "5884102"   "5889970"   "5892964"   "5911051"   "5914727"   "5915265"   "5937173").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/22 08:01
S12 5	1	09/768073	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/26 12:59

S12 6	286	(first and second) near2 ((graphics video) near (card adapter accelerator controller engine hardware chip chipset))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 13:18
S12 7	1	S126 and (low near2 resistance)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 09:03
S12 8	777	(low near2 resistance) near switch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 09:03
S12 9	6	S128 and ((graphics video) near (card adapter accelerator controller engine hardware chip chipset))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/12/02 09:05